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Date: 06.08.2021

TO WHOMSOEVER THIS MAY CONCERN

This is to certify that **Mr. Omkar Bhilare (Roll No: 191060901)**, B. E Department of Electrical Engineering, **Veermata Jijabai Technological Institute, Mumbai** has worked in the project titled **"FPGA Based Verification of SHAKTI Processors"** at Reconfigurable and Intelligent Systems Engineering (RISE) lab, Department of Computer Science and Engineering, IIT Madras as a part of his internship during **11th March 2021 to 31st July 2021.**

Prof. V. Kamakoti